



<b>Information Disclosure Statement By Applicant</b> (Use Several Sheets if Necessary)		Atty. Docket No. NVIDP234/P000825	Application No.: 10/633,004
		Applicant: Singh et al.	Group Art Unit: 2811
		Filing Date: 7/31/2003	

#### U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
A							
B							
C							
D							
E							
F							
G							
H							
I							
J							
K							
L							
M							
N							
O							
P							
Q							
R							
S							
T							
U							

#### Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
<u>V</u>	V	Horng, Tzyy-Sheng, "A Rigorous Study of Wire-Bonding and Via-Hole Effects on GaAs Field Effect Transistors", IEEE MTT-S Digest, 1995, pp. 785 - 788
	W	
	X	

Examiner HUNG VO Date Considered 03/15/05

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.